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Kumar , Ph.D., is Principal of K.L. University College of Engineering, K.L. University, Green Fields, Vaddeswaram, Andhra Pradesh, India. From 2006 to 2011 he served as Director, Sasi Institute of Technology and Engineering, Tadepalligudem, Andhra Pradesh, India.

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combinational circuit through Boolean reduction. Construct a simplified circuit and verify a truth table through measurement.

MATERIALS: [1] DC Power Supply [1] Logic Experimenter [1] Breadboard [1] 74LS32 OR Gate IC [1 ...

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